1st year

	Track	CLASSES	CREDITS
		French	4
	Soft and Human Skills	Conferences on aeronautics	1
		Communication	1
		Sport	1
		Math Fourier Analysis	2
	Mathematics	Math-Complex variable- Vector analysis	2
		Math probability/ Statistics	2
\vdash			
SEMESTER 1		Basis of programming/Matlab	1
EST	Programming	C programming	1
ŠEM		Microprocessor	2
	Digital electronics	VHDL design	3
		PBL : Digital electronics design	2
	Analog Electronics	Circuits	2
		Project analog electronics	2
		Analog practical	1
		Semi-conductor devices	1
		Filtering	1
		Transmission lines	1
		French	1
		Professional English	1
	Soft and Human Skills	Sport	1

		Conferences on aeronautics	1
		Junior Research project	2
2	Digital electronics	Digital electronics project	3
	Digital electronics	Front-end acquisition	3
SEMESTER			
ИES		Optoelectronics	2
SEN		Laser and optical fiber sensing techniques	1
	Optics/telecom	Practical Hyper / Opto	1
	Optics/telecom	Telecoms	2
		Signal for telecommunication	1
		Space telecoms	1
		Antennas	1.5
	RF	Passive RF	1
		Active RF circuits	1
		MEMS	2
		Hyper project	1.5
	Signal & image	Signal processing	1.5
	o.ga. aluge	Digital signal processing	1.5

2nd year

Track	CLASSES	CREDITS
	French	1
Soft and Human Skills	Conferences on aeronautics	1

Professional English Research project SOC Project analog electronics Architectures, interfacing and reliability of ES I Mobile autonomous platform project Synthesis strategy I FPGA conception project for signal processing VHDL design I Microprocessor power supply MOSFET Driver Circuits I EMC for SMPS EMC of integrated circuits I EMC of integrated circuits I Radar & remote Remote sensing project Radar equipment I Ref/opto Track CLASSES Six months internship in a company or a research lab. The internship will be concluded by an presentation in front of a jury composed by master ESECA and company tuttors.	1	SOIL AND MUNIAN SKINS	Internship presentation	2
Embedded Systems Embedded Systems Mobile autonomous platform project Synthesis strategy FPGA conception project for signal processing VHDL design Microprocessor power supply MOSFET Driver Circuits EMC of integrated circuits FEM modeling of integrate circuits Radar & remote Remote sensing project Radar equipment Photonics for HF PBL embedded optical systems Photonics for HF PBL embedded optical systems MIC Payload architecture Track CLASSES Six months integrate in a company or a research lab			Professional English	1
Embedded Systems Project analog electronics 2			Research project	3
Embedded Systems Project analog electronics 2				
Architectures , interfacing and reliability of ES I Mobile autonomous platform project Synthesis strategy FPGA conception project for signal processing VHDL design Microprocessor power supply MOSFET Driver Circuits EMC for SMPS EMC of integrated circuits I FEM modeling of integrate circuits Radar & remote Remote sensing project Remote sensing project Remote sensing project Photonics for HF PBL embedded optical systems MMIC Payload architecture Track CLASSES Six months integrable in a company or a research lab		Embedded Systems	SOC	2
Embedded Systems Mobile autonomous platform project 1			Project analog electronics	2
Synthesis strategy FPGA conception project for signal processing VHDL design Microprocessor power supply MOSFET Driver Circuits 1 EMC for SMPS EMC of integrated circuits 1 FEM modeling of integrate circuits 1 Radar & remote Remote sensing project Radar equipment Photonics for HF PBL embedded optical systems MMIC Payload architecture Track CLASSES Six months integrabin in a company or a research lab			Architectures , interfacing and reliability of ES	1
FPGA conception project for signal processing VHDL design 1 Microprocessor power supply 1 MOSFET Driver Circuits 1 EMC for SMPS 1 EMC of integrated circuits 1 FEM modeling of integrate circuits 1 Radar & remote Remote sensing project 2 Radar equipment 1 Photonics for HF 2 PBL embedded optical systems 2 MMIC 1 Payload architecture 1 Track CLASSES			Mobile autonomous platform project	1
WHDL design Microprocessor power supply Microprocessor power supply MOSFET Driver Circuits EMC for SMPS EMC of integrated circuits FEM modeling of integrate circuits 1 Remote sensing project Radar equipment Photonics for HF PBL embedded optical systems MMIC Payload architecture 1 Track CLASSES Six months integrable in a company or a research lab			Synthesis strategy	1
MOSFET Driver Circuits	33		FPGA conception project for signal processing	3
MOSFET Driver Circuits	STE		VHDL design	1
MOSFET Driver Circuits	MES			
Power management EMC for SMPS 1	SE		Microprocessor power supply	1
EMC of integrated circuits		Power management	MOSFET Driver Circuits	1
Remote sensing project Radar & remote Remote sensing project Radar equipment Photonics for HF PBL embedded optical systems AMMIC Payload architecture Track CLASSES Six months interpolar in a company or a research lab			EMC for SMPS	1
Remote sensing project Radar & remote Remote sensing project Radar equipment 1 Photonics for HF 2 PBL embedded optical systems 2 MMIC Payload architecture 1 Track CLASSES Six months internship in a company or a research lab			EMC of integrated circuits	1
Radar & remote Radar equipment 1			FEM modeling of integrate circuits	1
Radar & remote Radar equipment 1				
Radar equipment		Radar & remote	Remote sensing project	2
PBL embedded optical systems MMIC Payload architecture 1 Track CLASSES Six months interpship in a company or a research lab			Radar equipment	1
PBL embedded optical systems MMIC Payload architecture 1 Track CLASSES Six months interpship in a company or a research lab				
MMIC Payload architecture 1 Track CLASSES Six months interpship in a company or a research lab			Photonics for HF	2
MMIC Payload architecture 1 Track CLASSES Six months interpship in a company or a research lab		RF/opto	· · · · · · · · · · · · · · · · · · ·	2
Track CLASSES Six months internship in a company or a research lab			-	1
Six months internship in a company or a research lah				1
Six months internship in a company or a research lab. The internship will be concluded by an presentation in front of a jury composed by master ESECA and company tutors.		Track		
front of a jury composed by master ESECA and company tutors.	ER 2		·	20
S S	SEMEST	internsnip		30